

PIN INDEX	SIGNAL	I/O	FUNCTION	TYPE	WORKING RANGE	COMMENT
Power Supply						
1	VBAT	*	Main power supply	POWER	3.4V~4.20V	refer to page 27 of HE910_Hardware_User_Guide_r4.pdf
2	VBAT	*	Main power supply	POWER	3.4V~4.20V	
3	VBAT	*	Main power supply	POWER	3.4V~4.20V	
4	VBAT	*	Main power supply	POWER	3.4V~4.20V	
5	GND	*	Ground	POWER	*	refer to page 27 of HE910_Hardware_User_Guide_r4.pdf
6	GND	*	Ground	POWER	*	
7	GND	*	Ground	POWER	*	
8	*	*	*	*	*	Analog Audio pins for previous module. Only EAR+, EAR-, MIC+ and MIC- available on GE910 and CE910.
9	NC	*	*	*	EAR+	
10	NC	*	*	*	EAR-	
11	*	*	*	*	*	
12	*	*	*	*	*	
13	NC	*	*	*	MIC+	
14	NC	*	*	*	MIC-	
15	*	*	*	*	*	
16	*	*	*	*	*	
17	*	*	*	*	*	
SIM Card Interface						
18	SIMVCC	*	Power for the SIM	SIM CARD INTERFACE	1.8V/3V	refer to page 27 of HE910_Hardware_User_Guide_r4.pdf
19	SIMRST	O	External SIM signal-RESET	SIM CARD INTERFACE	1.8V/3V	
20	SIMIO	I/O	External SIM signal-Data I/O	SIM CARD INTERFACE	1.8V/3V	
21	SIMIN	I	External SIM signal-Presence(active low)	SIM CARD INTERFACE	CMOS 1.8V	
22	SIMCLK	O	External SIM signal - Clock	SIM CARD INTERFACE	1.8V/3V	
Trace						
23	RX_TRACE/SPI_MOSI	I/O	RX Data for debug monitor	Trace	CMOS 2.8V	working voltage depends on U2(MIC5365/6-2.8). Can be adjusted to 2~5V. Shared with SPI TX & RX
24	TX_TRACESPI_MISO	I/O	TX Data for debug monitor	Trace	CMOS 2.8V	
Prog. /Data + Hw Flow Control						
25	C103/TXD	I	Serial data input (TXD) from DTE	Prog. / Data + Hw Flow Control	CMOS 2.8V	working voltage depends on U2(MIC5365/6-2.8). Can be adjusted to 2~5V.
26	C104/RXD	O	Serial data output to DTE	Prog. / Data + Hw Flow Control	CMOS 2.8V	
27	C107/DSR	O	Output for Data set ready signal (DSR) to DTE	Prog. / Data + Hw Flow Control	CMOS 2.8V	
28	C106/CTS	O	Output for Clear to send signal (CTS) to DTE	Prog. / Data + Hw Flow Control	CMOS 2.8V	
29	C108/DTR	I	Input for Data terminal ready signal (DTR) from DTE	Prog. / Data + Hw Flow Control	CMOS 2.8V	
30	C125/RING	O	Output for Ring indicator signal (RI) to DTE	Prog. / Data + Hw Flow Control	CMOS 2.8V	
31	C105/RTS	I	Input for Request to send signal (RTS) from DTE	Prog. / Data + Hw Flow Control	CMOS 2.8V	
32	C109/DCD	O	Output for Data carrier detect signal (DCD) to DTE	Prog. / Data + Hw Flow Control	CMOS 2.8V	
33	*	*	*	*	*	*
34	*	*	*	*	*	*
35	*	*	*	*	*	*
Digital Audio Interface (CLK)						
36	DVI_CLK	I/O	Digital Audio Interface (CLK)	Digital Audio Interface	CMOS 2.8V	working voltage depends on U2(MIC5365/6-2.8). Can be adjusted to 2~5V.
37	ACD_IN1	AI	analog ADC input	Analog Interface	0 - 2.8V	Only available on HE910 and CE910
38	*	*	*	*	*	*
39	*	*	*	*	*	*
40	*	*	*	*	*	*
41	SPI_MRDY	I/O	MRDY	SPI interface	CMOS 2.8V	Only available on HE910
42	SPI_SRDY	I/O	SRDY	SPI interface	CMOS 2.8V	
43	SPI_CLK	I/O	CLK	SPI interface	CMOS 2.8V	Not available on GE910
44	SPI_CS_N(NC)	I/O	SPI SLAVE SELECT	SPI interface	CMOS 2.8V	Only available on DE910 and CE910
Miscellaneous Functions						
45	STAT_LED	O	Status indicator led	Miscellaneous Functions	CMOS 2.8V	working voltage depends on U2(MIC5365/6-2.8). Can be adjusted to 2~5V. Shared with GPIO1

